

(19)



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(11)

EP 1 215 823 A3

(12)

EUROPEAN PATENT APPLICATION

(88) Date of publication A3:
26.03.2003 Bulletin 2003/13

(51) Int Cl.7: H04B 1/707

(43) Date of publication A2:
19.06.2002 Bulletin 2002/25

(21) Application number: 01129277.8

(22) Date of filing: 12.12.2001

(84) Designated Contracting States:
AT BE CH CY DE DK ES FI FR GB GR IE IT LI LU
MC NL PT SE TR
Designated Extension States:
AL LT LV MK RO SI

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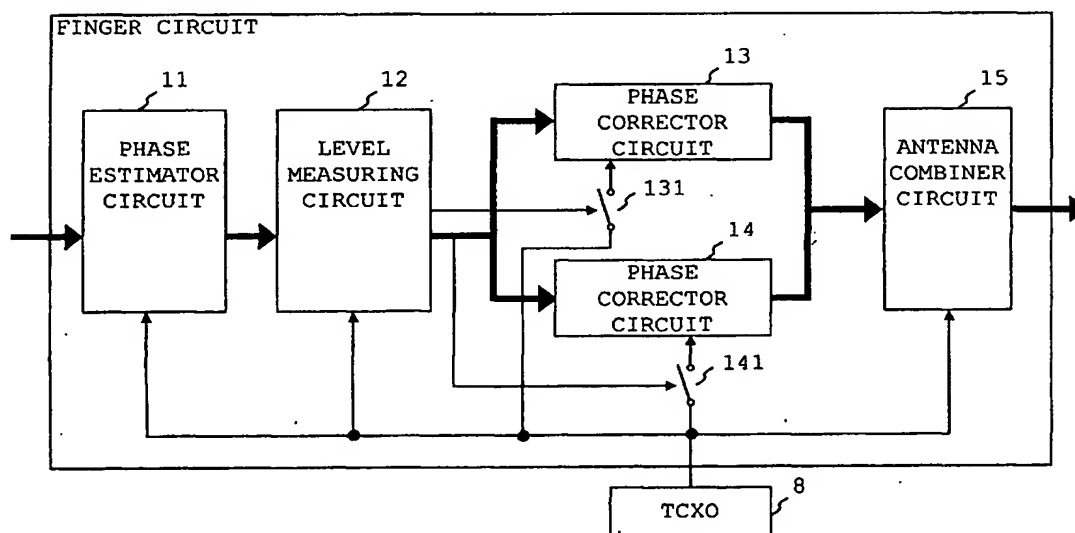
(30) Priority: 12.12.2000 JP 2000377363

(54) CDMA receiver terminal with reduced power consumption in diversity mode

(57) A CDMA receiver terminal comprises a finger circuit for correcting the phase of inverse spread data for signals from two antennas of a base station, and the finger circuit in turn has a level measuring circuit (12) for determining the validity of the two signals. The level

measuring circuit, when determining that any of the received signals from the two antennas of the base station is invalid, stops supplying an operating clock from a clock supply circuit (8) to a phase corrector circuit (13,14) which corrects the phase of the signal determined as invalid.

Fig. 5



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EUROPEAN SEARCH REPORT

Application Number
EP 01 12 9277

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Place of search MUNICH		Date of completion of the search 20 January 2003	Examiner Galli, P
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EPO FORM 1503 (03.02.92) (P04001)

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